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Έλαβε το διδακτορικό, το μεταπτυχιακό, και το δίπλωμα του από το Τμήμα Ηλεκτρονικών Μηχανικών και Μηχανικών Υπολογιστών του Πολυτεχνείου Κρήτης. Διδάσκει Αρχιτεκτονική Υπολογιστών και μαθήματα σχετικά με Υλικό Υπολογιστών (Computer Hardware). Αφού έλαβε το δίπλωμά του, εργάστηκε στη βιομηχανία στη σχεδίαση και υλοποίηση της 1ης γενιάς τεχνολογίας Bluetooth. Έλαβε χρηματοδότηση για την εμπορική αξιοποίηση της μεταπτυχιακής του έρευνας που αφορούσε την ανάπτυξη αλγορίθμων και συσκευής αναγνώρισης κινήσεων με εφαρμογή σε άτομα με κινητικά προβλήματα. Εργάστηκε ως διδακτικό προσωπικό στο Πολυτεχνείο Κρήτης, και ως επιστημονικός συνεργάτης στο Ινστιτούτο Τεχνολογίας και Έρευνας, Ηράκλειο, και στο Τεχνολογικό Εκπαιδευτικό Ινστιτούτο Κρήτης. Έχει πολλές επιστημονικές δημοσιεύσεις και 1 ευρεσιτεχνία στις ΗΠΑ. Έχει διατελέσει κριτής σε πλήθος επιστημονικών περιοδικών και συνεδρίων (ACM Transactions on Embedded Computing Systems, IEEE Transactions on VLSI Systems, IEEE Embedded Systems Letters, ELSEVIER Microelectronics Journal, IET Signal Processing, IEEE FPL, IEEE ReConFig, ARC, ReCoSoC).

ΕΡΕΥΝΗΤΙΚΑ ΕΝΔΙΑΦΕΡΟΝΤΑ

Επικεντρώνονται στην περιοχή του Υλικού (Computer Hardware), με εξειδίκευση στην Αναδιατασσόμενη Λογική (FPGAs), Ενσωματωμένα Συστήματα, Μικροεπεξεργαστές, Επιτάχυνση Εφαρμογών, Αξιοπιστία Κυκλωμάτων, και On-Chip Διασυνδέσεις. Ένα άρθρο της διδακτορικής του έρευνας που δημοσιεύθηκε στο επιστημονικό περιοδικό ACM Transactions on Reconfigurable Technology and Systems (ACM TRETTS) το 2011, είναι στη λίστα των 3 άρθρων που έχουν αναφερθεί τις περισσότερες φορές σε άλλες δημοσιεύσεις (ετεροαναφορές), από την αρχή έκδοσης του περιοδικού (2008). Το ACM TRETTS είναι το μοναδικό επιστημονικό περιοδικό στην περιοχή της Αναδιατασσόμενης Λογικής.

ΔΗΜΟΣΙΕΥΣΕΙΣ (2012-2018)

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2. D. Pnevmatikatos, **K. Papadimitriou**, T. Becker, P. Bohm, A. Brokalakis, K. Bruneel, C. Ciobanu, T. Davidson, G. Gaydadjiev, K. Heyse, W. Luk, X. Niu, I. Papaefstathiou, D. Pau, O. Pell, C. Pilato, M. D. Santambrogio, D. Sciuto, D. Stroobandt, T. Todman, E. Vansteenkiste, FASTER: Facilitating Analysis

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3. M.D. Grammatikakis, **K. Papadimitriou**, P. Petrakis, A. Papagrigoriou, G. Kornaros, I. Christoforakis, O. Tomoutzoglou, G. Tsamis, M. Coppola, Security in MPSoCs: A NoC Firewall and an Evaluation Framework, *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems (TCAD)*, special issue on Hardware Security and Trust, 34(8), pp. 1344-1357, 2015.
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Κεφάλαια Βιβλίων

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